Pf-2901/nec/us/mh

ABSTRACT OF THE DISCLOSURE

The present invention provides a detector for detecting at least

5   one kind of dependence in address between instructions executed by at

least a processor, the detector being adopted to detect a possibility of

presence of the at least one kind of dependence, wherein if the at least one

kind of dependence is present in fact, then the detector detects a possibility

of presence of the at least one kind of dependence, and if the at least one

10   kind of dependence is not present in fact, then the detector is allowed to

detect the at least one kind of dependence.